

L&T Press Release

Issued by Corporate Brand Management & Communications

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L&T Semiconductor Technologies Inks MoU with C-DAC *To Take C-DAC's Tech Solutions to Global Market*

Mumbai, August 28, 2024: L&T Semiconductor Technologies (LTSC), a pioneer in indigenous semiconductor design & development, has signed a Memorandum of Understanding (MoU) with the Centre for Development of Advanced Computing (C-DAC), an autonomous scientific entity under the Ministry of Electronics and Information Technology (MeiTY), Government of India.

The strategic collaboration is to drive indigenisation efforts and harness the collective expertise of both organisations to accelerate innovation and technological advancements in areas of mutual interest.

The MoU delineates a comprehensive framework for collaborative research, development, and training initiatives, with an emphasis on the creation of Make-in-India Integrated Circuit (IC) / System-on-Chip (SoC) and Electronics System Design & Manufacturing (ESDM) solutions for automotive, industrial, and energy applications.

The opportunities to strengthen India's economic foundation through indigenous semiconductor design & development are immense, particularly by reducing reliance on electronic imports from abroad. The collaborative efforts in developing specialised semiconductor technologies will be pivotal in advancing Indian processor cores, Intellectual Properties, and SoCs - the most cost-intensive components within the semiconductor value chain.

Congratulating both the organisations, **Dr Sunita Verma, Group Coordinator, (R&D E&IT) from the Ministry of Electronics and Information Technology (MeiTY)**, said: "The signing of this MoU between L&T Semiconductor Technologies and C-DAC indicates the Government's commitment to fostering public-private partnerships that drive innovation and economic growth. This collaboration not only underscores the importance of indigenisation in the semiconductor sector but also paves the way for India to take a leadership role on the global stage. We look forward to the transformative impact that this partnership will have on the nation's technological landscape."

Speaking on the occasion, **Mr Sandeep Kumar, Chief Executive - LTSC**, said: "This collaboration, led by LTSC, will create a powerful commercialisation programme for advanced technologies created by C-DAC in semiconductor design & development, embedded software, open-source OS, HPC and power systems. C-DAC's deep pipeline of indigenous IPs, including the VEGA processor, application design and FPGA validation will be turned into global product opportunities by LTSC. The joint efforts are anticipated to yield innovative products and solutions that will benefit diverse sectors of the Indian economy

while significantly enhancing the nation's technological capabilities and positioning India as a leader in each of the sectors.”

Mr Magesh E, Director General - C-DAC, highlighted that the collaboration will significantly boost the semiconductor industry and strengthen the Digital India RISC-V (DIR-V) ecosystem. “The partnership in developing Vega based indigenous ICs and SoCs is set to accelerate the vision of Atmanirbhar Bharat by enabling the development of cutting-edge products and solutions for automotive, industrial, ICT infrastructure and the energy sectors. With a shared vision and the capability to deliver world-class solutions, this collaboration is expected to break new ground in semiconductor and related domains,” he said.

Both LTSCT and C-DAC have expressed unwavering enthusiasm for the potential outcomes of this collaboration and reaffirm their commitment to working in concert to achieve a shared vision in meeting country dreams. LTSCT is committed to being a trusted partner to the Government of India, playing a crucial role in realising the vision of technological self-reliance on the global stage.

About LTSCT:

L&T Semiconductor Technologies Ltd. (LTSCT), a fully owned subsidiary of Larsen & Toubro, is a fabless Semiconductor multinational product company - focused on designing & delivering innovative solutions and comprehensive suite of services to its Global Customers. It provides Semiconductor Devices, Solutions and Technology partnership by helping customers realize energy efficient, high-performance systems to benefit from data, electrification and software defined technology trends. LTSCT has its R&D centres in Bangalore, Delhi & Chennai and Product Engineering & Sales divisions in Austin, Munich and Tokyo.

Background:

Larsen & Toubro is a USD 27 billion Indian multinational engaged in EPC Projects, Hi-Tech Manufacturing, and Services. It operates in over 50 countries worldwide. A strong, customer-focused approach and the constant quest for top-class quality have enabled L&T to attain and sustain leadership in its major lines of business for eight decades.

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